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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Shiro FUJIMA

BOX ISSUE FEE

Serial No. 09/528,446

Allowed April 3, 2001

Filed March 17, 2000

BATCH E47

SEMICONDUCTOR MEMORY DEVICE HAVING  
WORD LINES DRIVEN BY ROW SELECTING  
SIGNAL AND COLUMN SELECTING SIGNAL  
LINES ARRANGED PARALLEL TO EACH OTHER

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to PTOL-37 mailed April 3, 2001, replace  
the drawings originally filed with the accompanying new formal  
drawings.

Respectfully submitted,

YOUNG & THOMPSON

By

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May 4, 2001